



**Faculty of Electrical & Electronics Engineering
BEE 1213 Digital Electronics**

Name: _____ **ID:** _____

Section: _____ **Date:** _____

(Failed to complete all the particulars above will be penalized 2 marks)

QUIZ 4

Mapping CO,PO,Domain,KI: C01,P02,C3

C01: Apply various techniques for digital logic fundamental and simplification.
P3: Complex Overt Response

a) State the different between **latches** and **flip-flop** in *terms of clock signal*.

[2 marks]

b) The waveform shown in Figure 1 are applied to two different Flip-flops:

- (i) Positive edge triggered J-K
- (ii) Negative edge triggered J-K

Draw the Q waveform response for each of these flip-flops, assuming that Q initially HIGH. Draw your answer in **APPENDIX A**.

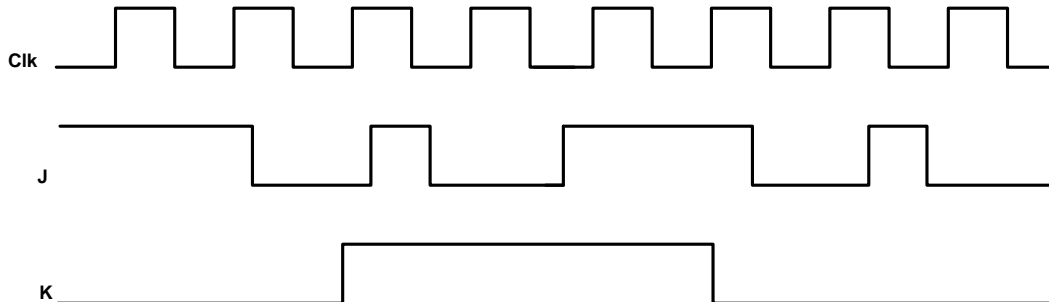


Figure 1

[8 marks]

APPENDIX A

