



Universiti
Malaysia
PAHANG
Engineering • Technology • Creativity

Faculty of Electrical & Electronics Engineering
BEE 1213 Digital Electronics

Name: _____

ID: _____

Section: _____

Date: _____

(Failed to complete all the particulars above will be penalized 2 marks)

QUIZ 2

Mapping CO,PO,Domain,KI: C01,P02,C3

C01: Apply various techniques for digital logic fundamental and simplification
C2: Understand

Given are the sets of Boolean function:

$$f_1(P, Q, R, S) = \sum_m (0,4,5,8,10,12,14)$$

- (i) Derive the truth table and SOP expression for the system.
- (ii) Use algebraic simplification to simplify the SOP expression acquired in (i).
- (iii) Implement the simplified expression into logic circuit diagram.

[10 marks]

ANSWER HERE: